



2813
PATENT
790001-2004 #10/Electra
12/23/02
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Hiroyuki YANO et al.
Serial No. : 09/870,085
Filed : May 30, 2001
For : **MANUFACTURING METHOD OF SEMICONDUCTOR
DEVICE**
Examiner : Thanhha Pham
Group Art Unit : 2813

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231, on December 1, 2002.

Grace L. Pan, Reg. No. 39,440
Name of Applicant, Assignee or Registered Representative


Signature

December 2, 2002
Date of Signature

RESPONSE

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the November 4, 2002 Office Action, which sets a thirty-day period for response, making this response due December 4, 2002, Applicants provisionally elect, without traverse, species I, upon which claims 1 to 4 and 27 to 30 read, for further prosecution, without prejudice, without admission, without surrender of subject matter, and without any intention to create any estoppel as to equivalents.